

PALM Intranet

Application Number

IDS Flag Clearance for Application 10687655

IDS Information

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
M844	2003-10-20	11	Y <input checked="" type="checkbox"/>	2006-05-09 18:27:01.0	dlui
<input type="button" value="Update"/>					

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		display\$6 and (variable and freq\$9 same divid\$6 same circuit\$6 and active same matrix "20030067434" and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 display\$6 same mode\$6 and pixel\$1 and substrate\$1 and var\$9 same freq\$6 same div\$9 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vary\$9 same freq\$9 same div\$6 same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:58
L2	3	display same mode and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:07
L3	3	display\$6 same mode\$6 and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:25
L4	34	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:09

EAST Search History

L5	16	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:09
L6	9	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:14
L7	5	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9 same divid\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:13
L8	2	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9 same divid\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:15
L9	8	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and divid\$6 and circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 23:06
L10	7	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and vari\$9 and circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:20
L11	2	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 same vari\$9 and circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:20
L12	3137	display and pixel\$1 and substrate\$1 and frequ\$8 same divi\$6 or vary\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vari\$6 same freq\$9 same divi\$6 same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:23

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L13	0	display\$6 and pixel\$1 and substrate\$1 and vary\$6 same freq\$8 same divi\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vari\$6 same freq\$9 same divi\$6 same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:24
L14	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and var\$6 same freq\$6 same div\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vary\$6 same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:28
L15	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (var\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and (vary\$6 same freq\$6 and div\$6 same circuit\$6) same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:30
L16	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (var\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:31

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L17	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (varY\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:32
L18	60	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (varY\$6 same freq\$6 and div\$6 same circuit\$6) and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:32
L19	60	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:36
L20	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:33
L21	8	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) and (first same (thin same film same transistor or TFT) same form\$6 and second same (thin same film same transistor or TFT) same form\$6 and substrate\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:34
L22	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 same div\$6 same circuit\$6) same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:36
L23	0	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (vary\$6 same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:37

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L24	10	display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:18
L25	61	display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:48
L26	11	display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and TFT	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:23
L27	11	display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:24
L28	10	display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:48
L29	11	display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:25
L30	89	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:48
L31	10	display\$6 and mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:49
L32	10	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:49
L33	44	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:54

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L34	13	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:50
L35	1	"6339422".pn. and (TFT or thin adj film adj transistor) and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:57
L36	39	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:55
L37	11	display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and transistor\$1 and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:56
L38	1	"6339422".pn. and transistor and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:57
L39	0	"6339422".pn. and matrix and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:57
L40	14	display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 15:58
L41	20704	display\$6 and (freq\$9 same divid\$6 same circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:45
L42	164	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same TFT)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:45
L43	199	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:46

EAST Search History

L44	51	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:46
L45	2	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:47
L46	24	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:35
L47	23	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu and substrate\$1 and pixel\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:51
L48	2	display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 same CPU)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:48
L49	1	"6339422".pn. and pixel\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:48
L50	3	display\$6 and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu and substrate\$1 and pixel\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:56
L51	1	"20030067434" and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:53
L52	201	display\$6 and variable and freq\$9 same divid\$6 same circuit\$6 and active same matrix	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:58
L53	50	display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:35

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L54	32	display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix and cpu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 16:58
L55	13	display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 17:12
L56	1	"5859626".pn. and Ic\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 17:28
L57	3	"20020036636"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:35
L58	0	"20020036636" and display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:36
L59	2	"20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:40
L60	0	"20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:36
L61	0	"20020036636" and display\$6 and freq\$9 same divid\$6 same control\$6 and cpu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:36
L62	2	"20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6 and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:40
L63	2	"20020036636" and freq\$9 same divid\$6 same circuit\$6 and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:44

EAST Search History

L64	2	"20010007432"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:42
L65	2	"20010007432" and substrate\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:43
L66	0	"20010007432" and substrate\$1 and control\$6 and cpu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:43
L67	0	"20010007432" and substrate\$1 and control\$6 and (cpu or microprocessor or controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:44
L68	3	"20020036636" and control\$6 and (cpu or microprocessor or controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:45
L69	0	"20020036636" and control\$6 and (cpu or microprocessor or controller) and substarte\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 19:45
L70	2	11-202290	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 20:43
L71	30	"202290"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 20:42
L72	3	"11202290"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 20:44
L73	5	display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and divid\$6 and circuit\$6) and diode\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 23:06

EAST Search History

S1	0	display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same thin same film same transistor same form\$6 same substrate\$1 and CPU and second same thin same film same transistor same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and divid\$6 same ratio same var\$6 same according same display\$6 same mode\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 13:05
S3	0	display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and divid\$6 same ratio same var\$6 same according same display\$6 same mode\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:59
S4	3	display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:21
S5	3	display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:34
S6	3	"20030063077"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:35

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S7	0	"20030063077" and mode same display\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:35
S8	2	"20030063077" and mode same display\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:50
S9	2	"20030063077" and mode same display\$6 and frequency\$6 same div\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 15:50
S10	0	display and pixel\$1 same substrate\$1 same charge same pumpo	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/25 12:08
S11	29	display and pixel\$1 same substrate\$1 same charge same pump	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 16:00
S12	16	display and pixel\$1 same substrate\$1 same charge same pump same driv\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 16:01